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UM-SJTU JOINT INSTITUTE

INTRO TO CIRCUITS

(VE 215)

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LABORATORY REPORT

LAB 3

TRANSIENT LAB

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| Date: 9 November 2019 |  |  |

1. **Introduction [1]**
   1. **Objectives**

* Apply the theory we learned on the step responses in first- and second-order circuits to series *RC* and *RLC* circuits, which we will build in the lab.
* Build a series *RC* circuit, observe its responses to input square wave signal of varied frequency, and explain them based on the theory we learned
* Relate the observed capacitor voltage and resistor voltage as functions of time to the pre-lab calculations
* Explain the changes of both output waveforms in response to the increase of the frequency of the input square wave signal
* Explain the amplitudes of the capacitor voltage and the resistor voltage related to the amplitude of the input square wave
* Build a series RLC circuit, observe the three types of its responses to input square wave signal, and relate them to the theory you have learned. For the under-damped/over-damped/critically damped response, compare the resistance in the circuit measured in the lab with the critical resistance you calculated in the pre-lab
* Build the simplest second-order circuit, an *LC* tank, and observe oscillations
  1. **Apparatus & Theoretical background**
     1. **First-order circuits**

Theoretically, the transient responses in electric circuits are described by differential equations. The circuits, whose responses obey the first-order differential equation

are called **first-order circuits**. Their responses are always monotonic and appear in the form of exponential function

A first-order circuit includes the effective resistance *R* and one energy-storage element, an inductor *L* or a capacitor *C*.

In an *RC* circuit, the time constant is

In an *LC* circuit, the time constant is

The **fall time** of a signal is defined as the interval between the moment when the signal reaches its 90% and the moment when the signal reaches its 10% level. We should note that the 10% level is reached between 2 and 3. Approximately, we can assume *fall time* ≈ 2.2 . After *t* = 5 , the exponent practically equals zero.

* + 1. **Second-order circuits**

Many circuits involve two energy-storing elements, both an inductor *L* and a capacitor *C*. Such circuits require a second-order differential equation description

thus they are called **second-order circuits**.

We will consider only second-order circuits with one inductor and one capacitor. The differential equation includes two parameters: the damping factor and the undamped frequency which are determined by the circuit and its components.

For example, in the series *RLC* circuit, which you will build and study in this lab,

while in the parallel *RLC* circuit,

Depending on the two parameters and , second-order circuits can exhibit three types of responses.

* + - 1. **The underdamped response**

If ,

where

The underdamped circuit response involves decaying oscillations, which may last for many periods or for less than one period, depending on the damping ratio , which for the series *RLC* circuit . Varying the values of *R*, *L*, *C*, affects the damping ratio .

* + - 1. **The critically damped response**

If ,

and the circuit has the critically damped response.

The critically damped response does not involve oscillations.

For the series RLC circuits, corresponds to or .

If and , then .

* + - 1. **The critically damped response**

If ,

where

In the series *RLC* circuits, the overdamped solution is obtained if the resistance is larger than the critical resistance, such that .

We should notice that the larger resistance corresponds to the longer delay, and even the faster decay has a much longer fall time than the critically damped response.

One of the most interesting features of series *RLC* circuits is that increasing the resistance above the critical value results in much longer fall time, or longer delays of responses in digital circuits. Among all monotonic responses, the critically damped is the fastest.

* + 1. **Apparatus**

The experimental setup of this experiment consists of DC source, protoboards, function generator, oscilloscope, resistors, slide rheostat and so on.

1. **Measurements [2]**
   1. **First-order circuit**

, are used in this part. First, we turn on the function generator. And we set a square wave at 1 and 100 Hz. Then, we apply it to the circuit as the input signal. Second, we monitor the input signal in Channel 1 and the output in Channel 2 of the oscilloscope. And finally complete the table.

For the **fastest** circuit response, we should set , and set the oscilloscope:

1. Vertical scale for the input signal 200mV/div;
2. Vertical scale for the output signal 200mV/div;
3. Horizontal scale 5ms/div;

For the **slowest** circuit response, we should set , and set the oscilloscope:

1. Vertical scale for the input signal 200mV/div;
2. Vertical scale for the output signal 50mV/div;
3. Horizontal scale 5ms/div;
   1. **Second-order circuit**

, , and are used in this part

1. On the function generator, we should set a square wave at **1** and **10 kHz** as the input signal.
2. We should vary to generate three kinds of plot on the oscilloscope. (Under-damped, critically damped, over-damped response).
3. We should observe and save the graph from the oscilloscope.
4. We should record: **Fall time** and **Rise time**, the time interval between the neighboring peaks, ∆*t*, and the resistance of the potentiometer, .
5. **Results & discussion**
   1. **First-order circuit**

According to the procedure described in 2.1., we can get Table 1 for the first-order as shown below.

|  |  |  |
| --- | --- | --- |
| The setting of the potentiometer corresponding to the … | **Fastest** circuit response | **Slowest** circuit response |
| **Peak-to-peak** voltage of the **input** square wave, [V] | 0.960 | 0.952 |
| **Peak-to-peak** voltage of the **output** square wave, [V] | 0.864 | 0.832 |
| **Period** of the **input** square wave, T [ms] | 10.0 | 10.0 |
| **Rise time** of the **output** waveform, [ms] | 0.320 | 1.89 |
| **Fall time** of the **output** waveform, [ms] | 0.280 | 1.88 |

Table 1. First-order circuit

Based on Table 1, we can calculate the time constant using the fastest fall time of the output waveform:

which corresponds to the . Theoretically, the value should be

Then, the relative error can be calculated as

We find that the relative error is huge. Considering the way how we operate, we may contribute it to the precision of the instrument. Moreover, when time is small, it is hard to measure it accurately. Besides, the wave on the oscillator has width, which will increase the difficulty of measuring.

We can calculate the time constant using the slowest fall time of the output waveform:

which corresponds to the . Theoretically, the value should be

Then, the relative error can be calculated as

We find that the relative error is huge. Similarly, considering the way how we operate, we may contribute it to the precision of the instrument. Moreover, when time is small, it is hard to measure it accurately. Besides, the wave on the oscillator has width, which will increase the difficulty of measuring.

* 1. **Second-order circuit**

According to the procedure described in 2.2., we can get Table 2 for the second-order as shown below.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
|  | Resistance, | Rise time, [ms] | Fall time, [ms] | Time interval, |
| Underdamped | 34.7 | 1780 | 1710 | 0.1 |
| 201.6 | 2400 | 2380 | 0.1 |
| Critically damped | 984 | 3600 | 3570 | 0.1 |
| Overdamped | 2233 | 6840 | 6340 | 0.1 |
| 5190 | 13200 | 13100 | 0.1 |

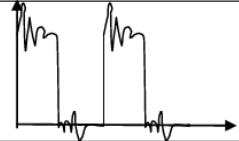
Table 2. Second-order circuit

Based on Table 2, we can get the resistance for critically damped is 984 .

According to the theory we learnt, when it is critically damped, , then we may get that

The relative error is

which is very huge. First, we may think this is because of the wrong connection of the circuit. But the circuit is approved by the TA. And when we see the value of the overdamped cases, we may think that we may find the critically damped point wrong because the resistance for the overdamped cases is larger than , which is correct and the difference of resistance for critically damped and overdamped is also huge. Therefore, we may think that we find the critically damped point wrong.

However, we forgot to save the graph from the oscilloscope, which is a huge pity for this experiment. Theoretically, we should have the graphs like below for the underdamped, critically damped and overdamped cases.

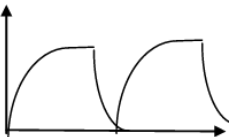
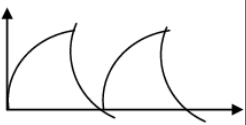
Figure 1. Underdamped [3]

Figure 2. Critically damped [3]

Figure 3. Overdamped [3]

1. **Conclusions [1]**

In this experiment, we apply the theory we learned on the step responses in first- and second-order circuits to series *RC* and *RLC* circuits, which we will build in the lab; we build a series *RC* circuit, observe its responses to input square wave signal of varied frequency, and explain them based on the theory we learned; we relate the observed capacitor voltage and resistor voltage as functions of time to the pre-lab calculations; we explain the changes of both output waveforms in response to the increase of the frequency of the input square wave signal; we explain the amplitudes of the capacitor voltage and the resistor voltage related to the amplitude of the input square wave; we build a series RLC circuit, observe the three types of its responses to input square wave signal, and relate them to the theory you have learned. For the under-damped/over-damped/critically damped response, compare the resistance in the circuit measured in the lab with the critical resistance you calculated in the pre-lab; we build the simplest second-order circuit, an *LC* tank, and observe oscillations.

First, we built the first-order circuit *RC* and recorded peak-to-peak voltage of the Input square wave, peak-to-peak voltage of the Output square wave, period of the Input square wave, rise time of the output waveform and fall time of the output waveform. The relative error we get in the experiment are around 20-25%, and we contribute it to the precision of the instrument. Moreover, when time is small, it is hard to measure it accurately. Besides, the wave on the oscillator has width, which will increase the difficulty of measuring.

Then, we built the second-order *RLC* circuit and recorded the detailed valued of resistors, rise time, fall time and time interval for underdamped, critically damped and overdamped cases. The critically damped point we get is wrong in the lab. Besides, we forgot to save the graph form the oscilloscope, which is a really pity.

1. **References**

[1] Lab 3\_Transient Lab\_Manual.pdf

[2] Lab 3\_Transient Lab\_DataSheet.pdf

[3] Physics Lab Repot of Nanchang University

<https://wenku.baidu.com/view/fc3c8a7ecdbff121dd36a32d7375a417866fc1a4.html>